



501.30980CC7

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: N. KANEKAWA, et al

Serial No.: 10/786,008

Filed: February 26, 2004

For: ELECTRONIC CIRCUIT PACKAGE

Group: 2818

Examiner: V. Nguyen

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.97 & 1.98**

MS Amendment

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 14, 2005

Sir:

In the matter of the above-identified application, applicants are submitting herewith a copy the document listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This information disclosure statement is being submitted before the mailing date of a first office action on the merits.

The document listed on the attached form equivalent to Form PTO-1449 is in the English language.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of MATTINGLY, STANGER, MALUR & BRUNDIDGE, P.C., Deposit Account No. 50-1417 (501.30980CC7) please credit any excess fees to such deposit account.

Respectfully submitted,



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MATTINGLY, STANGER, MALUR & BRUNDIDGE, P.C.

FORM PTO-1449 U.S. Department of
Commerce Patent and Trademark Office

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use several sheets if necessary)

ATTY. DOCKET NO.

501.30980CC7

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N. KANEKAWA, et al

FILING DATE

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT
					YES NO
1 2 5 8 4 6 6	10/89	Japan			Eng. Trans.

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Data Book "Hitachi IC Memory Data Book", HITACHI, HB56A49 Series, 4194304 – word x 9-bit High Density Dynamic RAM Module, pp. 1-8 (English Translation), 1979.
	Data Book "Hitachi IC Memory Data Book 2", HITACHI, HB56D51236 Series 524288-word x 36-bit High Density Dynamic RAM Module, pp. 1-10 (English Translation), 1988.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

(Form PTO-1449 [6-4])